



MSTronic Co., Ltd.

2F, 12, Gongshang Rd., Wugu District, New Taipei City, 248 Taiwan

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SPECIFICATION

MIT-65G-4856D-BT

DC/DC Gigabit PoE Injector

802.3bt

1. INPUT :

1.1 Input Voltage: 40Vdc to 72Vdc Normal = 56V

1.2 Input Current: 2.6A at 40Vdc @ Full Load

1.9 A at 56Vdc @ Full Load

1.5 A at 72Vdc @ Full Load

***Note:** isolated design, both positive and negative input are all available.

2. OUTPUT :

2.1 Output Voltage & Current:

OUTPUT	56V
Max. load	1.61A
Power	90W
Min. Load	0.01A
Load reg. %	5%
Line reg. %	1%
Ripple %	1%
Noise %	1%

TOTAL POWER :90W

Note 1: Noise bandwidth is from DC to 20Mhz. Ripple & Noise is measured by Paralleling a 0.1uF metalize capacitor on the test point.

3. EFFICIENCY : 80% min at Full Load , 40Vdc ~ 72Vdc Input Voltage

4. PROTECTION

4.1 Short Circuit Protection

Output Short GND Terminal will not damage the Power Supply and will Auto-Reset.

4.2 Input with Fuse Protection.



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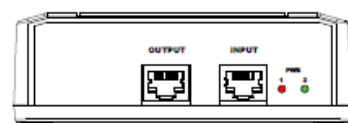
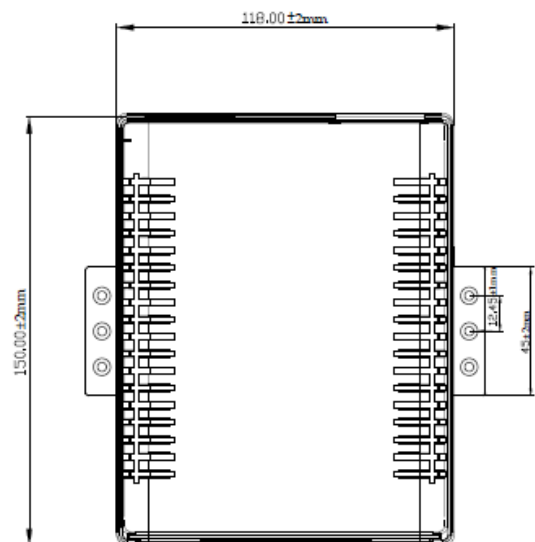
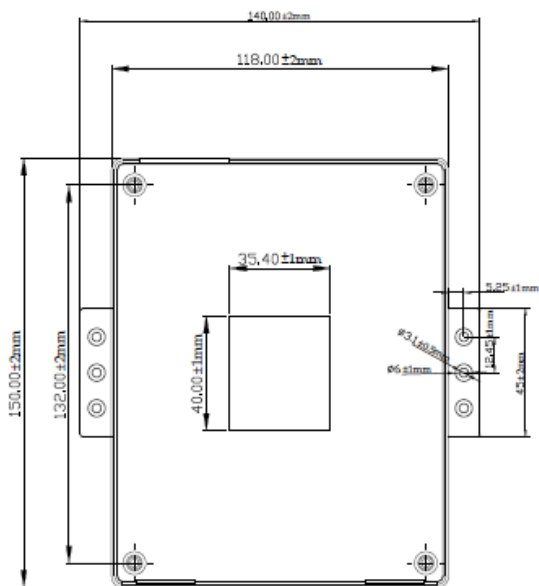
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- 4.3 Safety Standard : meet EN62368-1.
- 4.4 EMC : meet EN55032 Class B, FCC Class B ,
- 4.5 Over Current : 802.3BT Class 8 (96.6W)@All DC Input F.L
- 4.6 Input Reverse Protection-----NO Work
- 4.7 INPUT LOW Voltage Protection Point : $33.2V \pm 0.3V$
- 4.8 OUTPUT Over Voltage Protection Point : $59V \pm 0.5V$

5. GENERAL DESCRIPTION

- 5.1 Operation Temperature: -40 - +70 Degree
- 5.2 Storage Temperature: -40 - +85 Degree
- 5.3 Operation Humidity: 5% - 90%
- 5.4 Cooling: Free air cooling
- 5.5 SIZE : 140*150*40 (L*W*H)m/m





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6. Indication :

6.1 LED1(RED) Indicate POE Output no valid detection.

6.2 LED2(GREEN) Indicate POE Output **802.3BT** at PD detected

7. CHANNEL CONNECTORS & PINOUT :

RJ-45 Input (only Data)		
Pin	Symbol	Description
1	BI_DA+	Data Pair A+
2	BI_DA-	Data Pair A-
3	BI_DB+	Data Pair B+
4	BI_DC+	Data Pair C+
5	BI_DC-	Data Pair C-
6	BI_DB-	Data Pair B-
7	BI_DD+	Data Pair D+
8	BI_DD-	Data Pair D-
9	Shield	Connector shielding

RJ-45 Output (Data & Power)		
Pin	Symbol	Description
1	BI_DA+, Vo-	Data Pair A+, Output power (-)
2	BI_DA-, Vo-	Data Pair A-, Output power (-)
3	BI_DB+, Vo+	Data Pair B+, Output power (+)
4	BI_DC+, Vo+	Data Pair C+, Output power (+)
5	BI_DC-, Vo+	Data Pair C-, Output power (+)
6	BI_DB-, Vo+	Data Pair B-, Output power (+)
7	BI_DD+, Vo-	Data Pair D+, Output power (-)
8	BI_DD-, Vo-	Data Pair D-, Output power (-)
9	Shield	Connector shielding

Note : the model is isolated design.



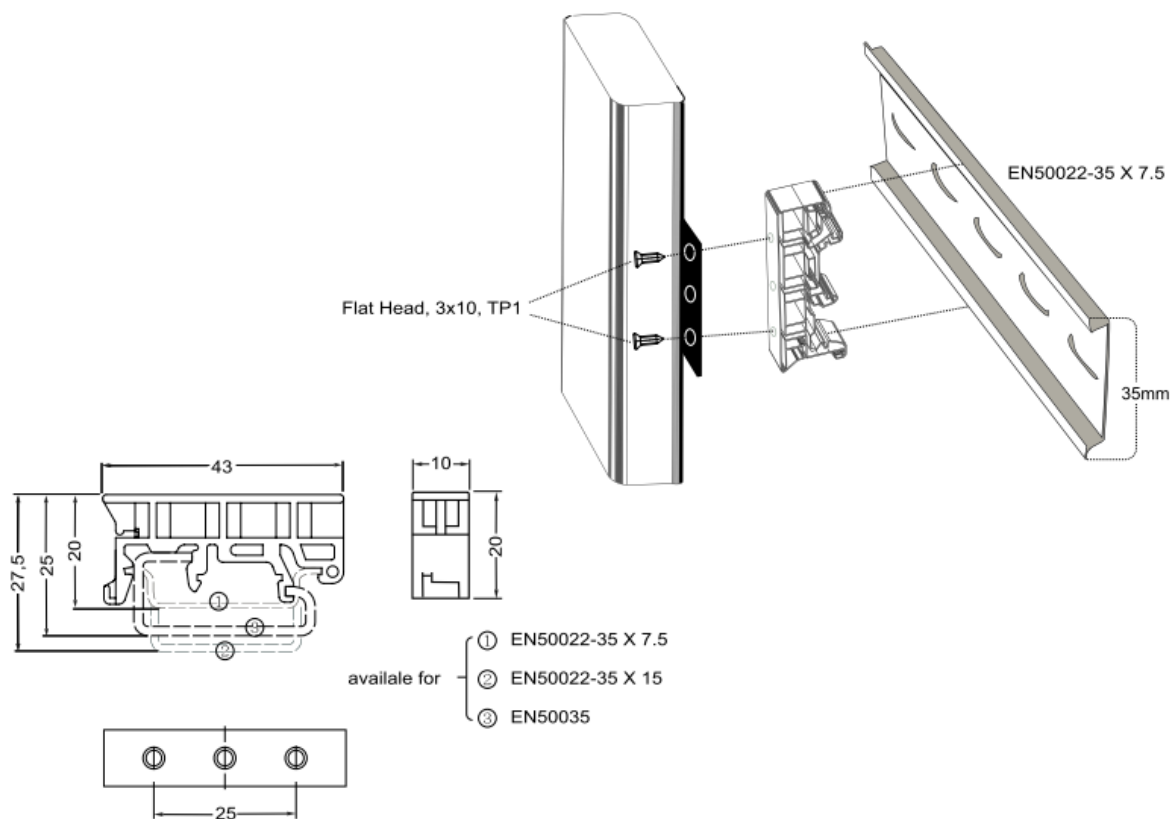
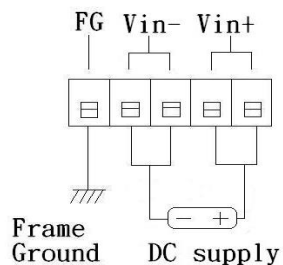
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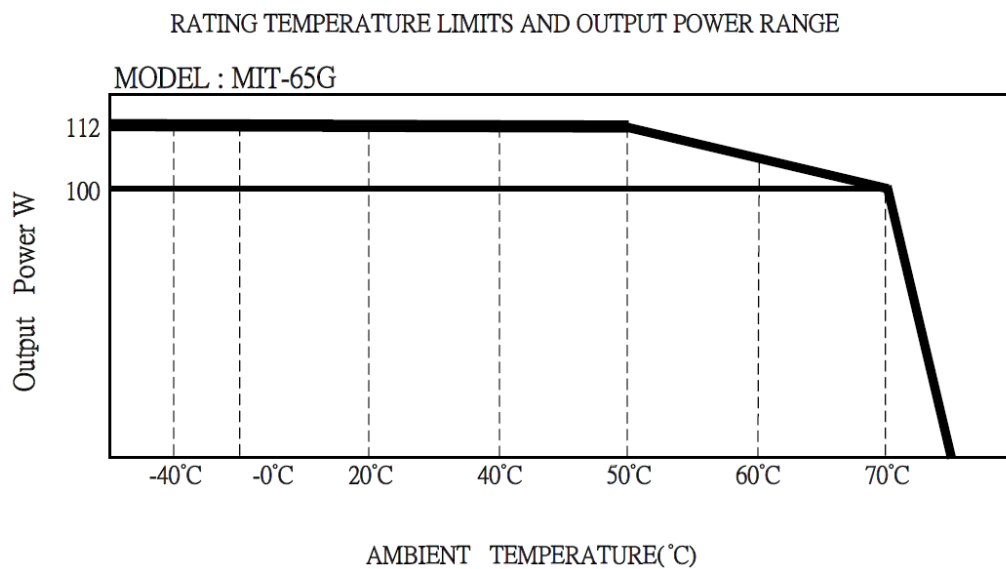
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Output up to 112W at 50C. De-rate output once above 50C, 100W maximum when 70C



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ELECTRONICS TESTING CENTER, TAIWAN

Report No.: 18-12-MAT-088-01

EMC TESTING DEPARTMENT

Page: 1/30



CONFORMANCE TEST REPORT FOR EN 55032 / EN 55024

Report No.: 18-12-MAT-088-01

According to:

- Electromagnetic Compatibility Directive: 2014/30/EU
- Low Voltage Directive: 2014/35/EU
- Radio Equipment Directive: 2014/53/EU
- Machinery Directives: 2006/42/EC

Applicant: MSTronic Co., Ltd.
Product: Power supply
Model: MIT-65G-4856PNN, MIT-65G-4856PNR, MIT-65G-4856D-BT,
MIT-65G-4856D1N, MIT-65G-4856D2N
Manufacturer: MSTronic Co., Ltd.
Brand Name: 
Date test item received: 2018/12/22
Date test campaign completed: 2019/05/17
Date of issue: 2019/05/28

The test result only corresponds to the tested sample. It is not permitted to copy this report, in part or in full, without the permission of the test laboratory.

Total number of pages of this test report: 30 pages

Total number of pages of this test photos: 10 pages

Test Engineer	Checked By	Approved By
		
Smith Hsu	Vincent Chang	Eric Chen

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Laboratory Introduction: Electronics Testing Center, Taiwan is recognized, filed and mutual recognition arrangement as following:

- ① ISO/IEC 17025 : BSMI, TAF, NCC, NVLAP, CBTL(JQA)
- ② Filing : FCC, ISCED(IC), VCCI
- ③ MRA : Australia, New Zealand, Singapore, Japan, ilac TAF
- ④ FCC Registration Number: TW0371, TW2223