



MSTronic Co., Ltd.

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SPECIFICATION

DIN-POE11-15456BDN

DC/DC Gigabit PoE Injector

1. INPUT :

1.1 Input Voltage: DC : 66Vdc to 154Vdc

1.2 Input Current : 1.07A at 66Vdc
0.46A at 154Vdc

2. OUTPUT :

2.1 Output Voltage & Current:

OUTPUT	56V
Max. load	1.07A
Power	60W Max.
Min. load	0A
Load reg. %	5%
Line reg. %	1%
Ripple %	1%
Noise %	1%

Note 1: Noise bandwidth is from DC to 20Mhz. Ripple & Noise is measured by Paralleling a 0.1uF metalize capacitor on the test point.

3. EFFICIENCY : 85% min at Full Load , 66Vdc ~ 154Vdc Input Voltage

4. PROTECTION :

4.1 Short Circuit Protection :

Output Short GND Terminal will not damage the Power Supply. The output current will auto-Recover when Load status back to normally.

4.2 Input with Fuse Protection. at P.C.B

4.3 Input Reverse will not damage the Power Supply: PWR Turn---off





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4.4 Input Over low Voltage Protection : 62V ~ 64V Turn-off Input

4.5 Surge protection : 8.5KV.

4.6 Indicator (on output RJ45)

LED1 (upper) GREEN indicates POE Output –OK (4,5/7,8)

LED2 (lower) (for 1236, unused)

4.7 PWR indicator: GREEN indicates PWR – OK

5. GENERAL DESCRIPTION

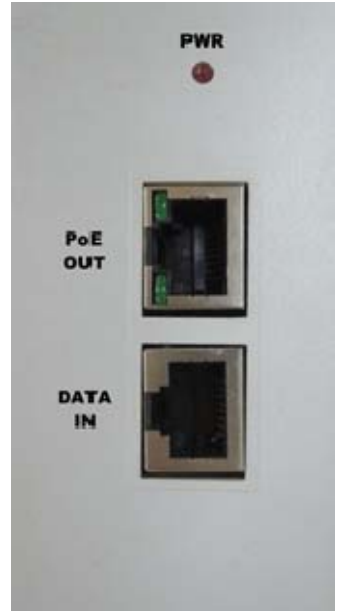
5.1 Operation Temperature: -40 - +70 Degree

5.2 Storage Temperature: -40 - +85 Degree

5.3 Operation Humidity: 5% - 90%

5.4 Cooling: Free air cooling

5.5 SIZE : 46 x 125 x 102 mm(W*H*D)



5. RJ45 pin assignment: (10M/100M/1000M)

RJ-45 Input (Data Only)			RJ-45 Output (Data & Power)	
Pin	Symbol	Description	Symbol	Description
1	BI_DA+	Data Pair A+	BI_DA+	Data Pair A+
2	BI_DA -	Data Pair A-	BI_DA -	Data Pair A-
3	BI_DB +	Data Pair B+	BI_DB +	Data Pair B+
4	BI_DC +	Data Pair C+	+Vdc + BI_DC +	power(+)+Data Pair C+
5	BI_DC -	Data Pair C-	+Vdc + BI_DC -	power(+)+Data Pair C-
6	BI_DB -	Data Pair B-	BI_DB -	Data Pair B-
7	BI_DD +	Data Pair D+	-Vdc + BI_DD +	power(-)+Data Pair D+
8	BI_DD -	Data Pair D-	-Vdc + BI_DD -	power(-)+Data Pair D-

Note : the model is isolated design, the output +/- or input +/- can be shorted to ground (FG).

6. CONNECTOR FIGURE :

Input : HD-515RC (P2)

1	2	3	4	5
FG	VI-	VI-	VI+	VI+



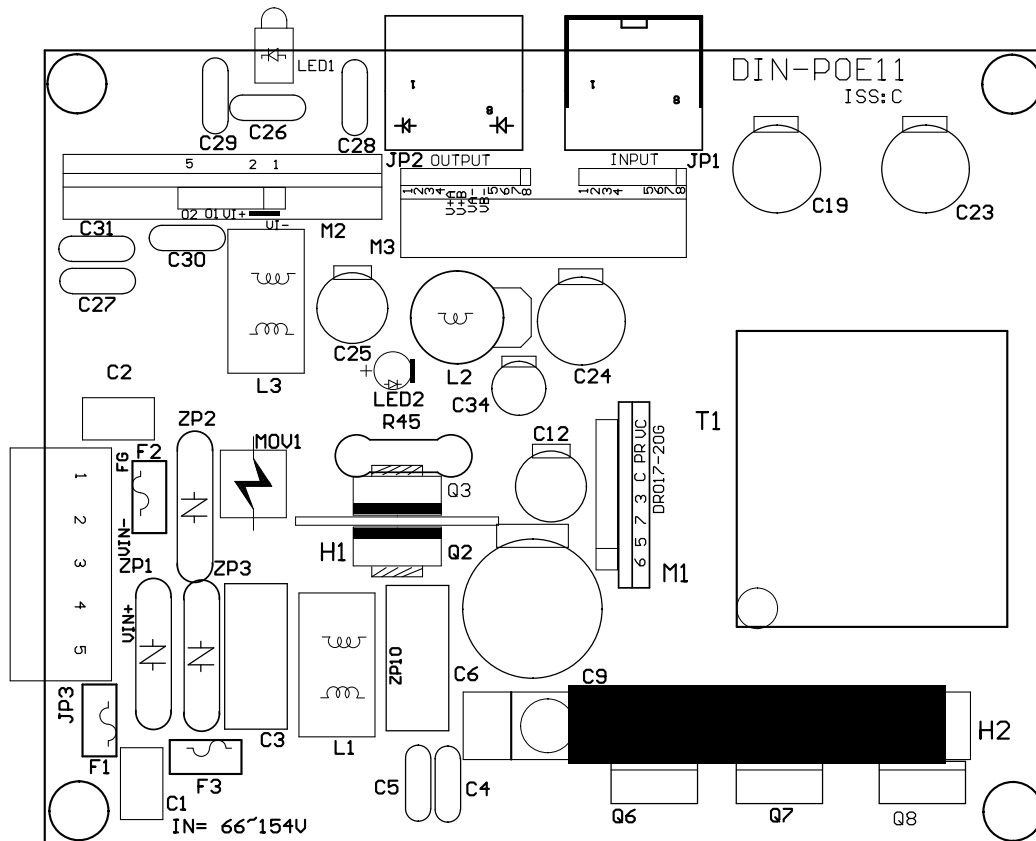
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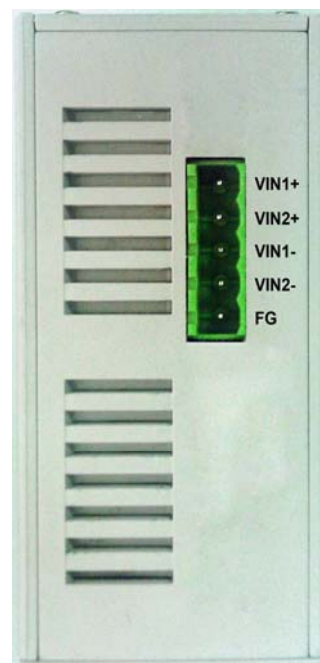
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8. CONNECTOR FIGURE :

Input : HD-515RC (P2)

Allow two inputs for redundant.





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