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SPECIFICATION

DIN-POE10-2456D1Nx2

DC/DC Gigabit PoE Injector

Single Signature LTPOE++

1. INPUT :

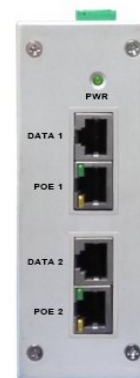
- 1.1 Input Voltage: DC 18V ~ 57V
- 1.2 Input Current:
 - 5.60A at 18Vdc @F.L 90W
 - 4.20A at 24Vdc @F.L 90W
 - 1.75A at 57Vdc @F.L 90W

2. OUTPUT :

- 2.1 Output Voltage & Current:

OUTPUT	POE 1	POE 2
Voltage	+56V	+56V
Max. load	1.6A	1.6A
Power	90W Max(*)	90W Max(*)
Min. Load	0A	0A
Load reg. %	5%	5%
Line reg. %	1%	1%
Ripple %	1%	1%
Noise %	2%	2%

TOTAL POWER : 90W



- 3. EFFICIENCY : 90% min. at DC 24V Input @F.L

4. PROTECTION

- 4.1 Short Circuit Protection

Output short will not damage the model, it will auto-recover when load back to normal.

- 4.2 Over Current Limits : 120% ~ 130% @18V ~ 57Vdc



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4.3 Input with Fuse Protection

4.4 Input Reverse Protection-----NO Work

4.5 Indicators:

The PWR LED: light GREEN means input power OK.

The LEDs on RJ45s: light YELLOW when power ready, no output yet,
light GREEN means POE Output –OK

5. GENERAL DESCRIPTION

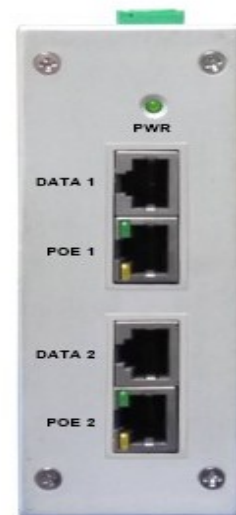
5.1 Operation Temperature: -40 - +70 Degree

5.2 Storage Temperature: -40 - +85 Degree

5.3 Operation Humidity: 5% - 90% (40 Degree)

5.4 Cooling: Free air cooling

5.5 SIZE 46 x 125 x 102 mm (L*W*H)



6. CONNECTION : RJ45 pin assignment: @1000MBASE

RJ-45 Input (Data Only)			RJ-45 Output (Data & Power)	
Pin	Symbol	Description	Symbol	Description
1	BI_DA+	Data Pair A+	-Vdc+BI_DA+	power(-)+Data Pair A+
2	BI_DA-	Data Pair A-	-Vdc+BI_DA-	power(-)+Data Pair A-
3	BI_DB+	Data Pair B+	+Vdc+BI_DB+	power(+)+Data Pair B+
4	BI_DC+	Data Pair C+	+Vdc+BI_DC+	power(+)+Data Pair C+
5	BI_DC-	Data Pair C-	+Vdc+BI_DC-	power(+)+Data Pair C-
6	BI_DB-	Data Pair B-	+Vdc+BI_DB-	power(+)+Data Pair B-
7	BI_DD+	Data Pair D+	-Vdc+BI_DD+	power(-)+Data Pair D+
8	BI_DD-	Data Pair D-	-Vdc+BI_DD-	power(-)+Data Pair D-

Note :

1. the model is isolated design, the output +/- or input +/- can be shorted to ground (FG).
2. The model with Single Signatures LTPoE++ output, make sure your end device meet the LTPD++ protocol.

7. CONNECTOR FIGURE :

Input : HD-515RC (P2)

Allow two inputs for redundant.

