



MSTronic Co., Ltd.

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SPECIFICATION

DIN-DC95-56DBN

DC/DC Gigabit PoE Injector

802.3bt compliant

1. INPUT :

- 1.1 Input Voltage: DC : 11V ~ 60V
- 1.2 Input Current :
 - 9.86A @11Vin at full load
 - 4.446 @24Vin at full load
 - 2.26A @48Vin at full load
 - 1.84A @60Vin at full load

2. OUTPUT :

2.1 Output Voltage & Current:

OUTPUT	56V
Max. load	1.67A
Power	95W Max.
Min. load	0A
Load reg. %	5%
Line reg. %	1%
Ripple %	1%
Noise %	1%



Note 1: Noise bandwidth is from DC to 20Mhz. Ripple & Noise is measured by Paralleling a 0.1uF metalize capacitor on the test point.

3. EFFICIENCY : 90% min 34V ~ 60V, 86%min 11V ~ 33V dc Input Voltage at Full Load

4. PROTECTION

4.1 Short Circuit Protection

Output Short GND Terminal will not damage the Power Supply and will auto-Reset.

4.2 Input with Fuse Protection.



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- 4.3 Over Current : 802.3bt Class 8 (96.6W)@All DC Input F.L
- 4.4 Input polarity Reverse Protection----- will not damage the Power Supply, NO work.
- 4.5 Input Over low Voltage Protection : 8V ~ 9V Turn-off Input
- 4.6 Surge protection on data input ports:

5.0 Indicators (on front panel, output RJ45)

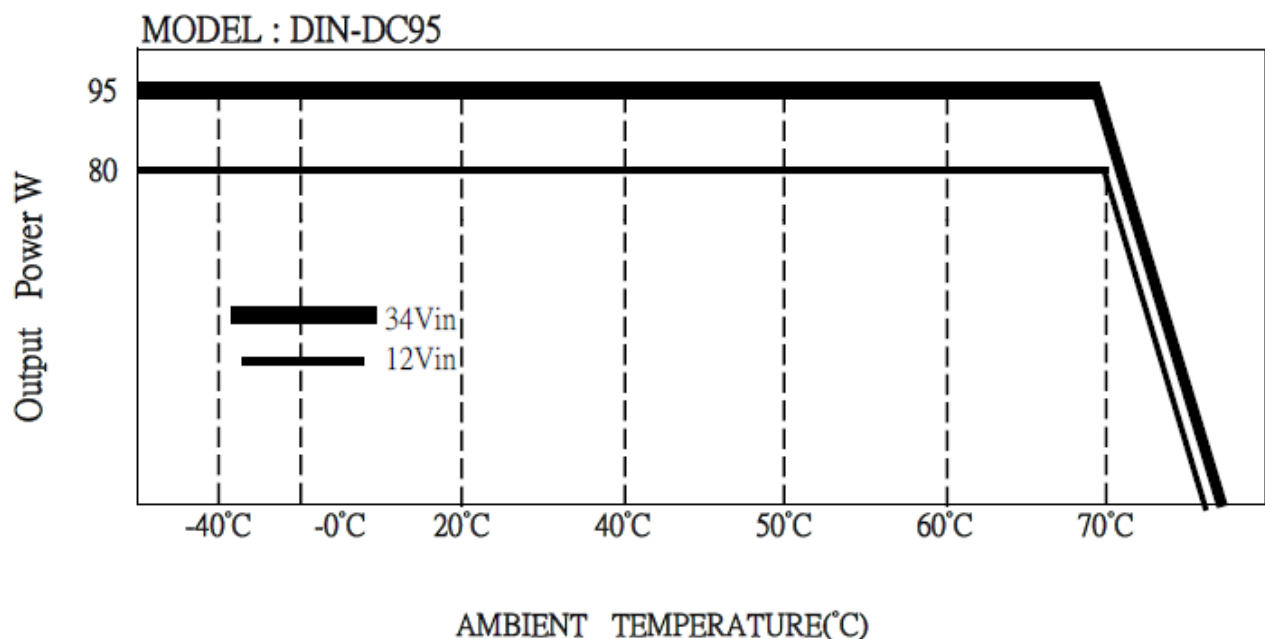
LED1 indicates PWR OK

RJ45 GREEN indicates PSE 802.3bt Output –OK

6. GENERAL DESCRIPTION

- 6.1 Operation Temperature: -40 - +70 C
- 6.2 Storage Temperature: -40 - +85 C
- 6.3 Operation Humidity: 5% - 90%
- 6.4 Cooling: Free air cooling
- 6.5 SIZE : 66 x 125 x 102 mm (W*H*D)

RATING TEMPERATURE LIMITS AND OUTPUT POWER RANGE





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7. RJ45 pin out assignment: (10M/100M/1000M/5000M)

RJ-45 Input (Data Only)			RJ-45 Output (Data & Power)	
Pin	Symbol	Description	Symbol	Description
1	BI_DA +	Data Pair A+	-Vdc +BI_DA +	power(-)+Data Pair A+
2	BI_DA -	Data Pair A-	-Vdc +BI_DA -	power(-)+Data Pair A-
3	BI_DB +	Data Pair B+	+Vdc +BI_DB +	power(+)+Data Pair B+
4	BI_DC +	Data Pair C+	+Vdc + BI_DC +	power(+)+Data Pair C+
5	BI_DC -	Data Pair C-	+Vdc + BI_DC -	power(+)+Data Pair C-
6	BI_DB -	Data Pair B-	+Vdc +BI_DB -	power(+)+Data Pair B-
7	BI_DD +	Data Pair D+	-Vdc + BI_DD +	power(-)+Data Pair D+
8	BI_DD -	Data Pair D-	-Vdc + BI_DD -	power(-)+Data Pair D-

Note : the model is isolated design.

8. CONNECTOR FIGURE :

Input : 5EHDRM-5P (P1)

Allow two inputs for redundant.(VI1+,VI2+)

