



MSTronic Co., Ltd.

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SPECIFICATION

DIN-POE9-56PNN

AC/DC Gigabit PoE Passive Injector

1. INPUT :

- 1.1 Input Voltage: AC : 100V ~350V @120V/230V
- 1.2 Input Frequency : 47 ~ 63Hz
- 1.2 Input Current : 0.85A @120Vac at full load
0.44A @230Vac at full load

2. OUTPUT :

- 2.1 Output Voltage & Current:

OUTPUT	56V
Max. load	1.07A
Power	60W Max.
Min. load	0A
Load reg. %	5%
Line reg. %	1%
Ripple %	1%
Noise %	2%



Note 1: Noise bandwidth is from DC to 20Mhz. Ripple & Noise is measured by Paralleling a 0.1uF metalize capacitor on the test point.

- 3. EFFICIENCY : 82% min at Full Load , 120Vac Input Voltage

4. PROTECTION

- 4.1 Short Circuit Protection

Output Short GND Terminal will not damage the Power Supply and will auto-Reset.

- 4.2 Input with Fuse Protection.

- 4.3 Safety Standard : meet UL1950, CSA 22.2 &TUV EN60950-1.



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- 4.4 EMC : meet FCC Class B , EN55022 Class B
- 4.5 Over Current : 120% ~ 150% @ 100Vac~350Vac Input F.L
- 4.6 Input polarity Reverse Protection-----NO output
- 4.7 Output Over Voltage Protection : 59V ~ 65V Turn-off
- 4.8 Surge protection on data input ports:

Operating Voltage	Data 5V
Clamping Voltage	Data 16.5V (@I PP =5A, t p =8/20μs, I/O pin to GND)
Peak Pulse Current	20A (tp=8/20μs)
Pin Protected	All 8 pin protected
Max. Shut Capacitance	<3pF (VR = 0V, f = 1MHz, I/O pin to GND) < 1.5 pF (VR = 0V, f = 1MHz, Between I/O pins)
IEC COMPATIBILITY (EN61000-4)	IEC61000-4-2 (ESD) ±15kV (air), ±8kV (contact) IEC61000-4-4 (EFT) 40A (5/50ns) IEC61000-4-5 (Lightning) 20A (8/20μs)

5.0 Indicators (on front panel, output RJ45)

LED1 (upper) RED indicates PWR – Ready

LED2 (lower) GREEN indicates POE Output –OK

(when LED2 light on, LED1 turn off at the same time)

6. GENERAL DESCRIPTION

- 6.1 Operation Temperature: -40 - +70 Degree
- 6.2 Storage Temperature: -40 - +85 Degree
- 6.3 Operation Humidity: 5% - 90%
- 6.4 Cooling: Free air cooling
- 6.5 SIZE : 46 x 125 x 102 mm(W*H*D)



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7. RJ45 pinouts assignment: (10M/100M/1000M)

RJ-45 Input (Data Only)			RJ-45 Output (Data & Power)	
Pin	Symbol	Description	Symbol	Description
1	BI_DA +	Data Pair A+	-Vdc +BI_DA +	power(-)Data Pair A+
2	BI_DA -	Data Pair A-	-Vdc +BI_DA -	power(-)Data Pair A-
3	BI_DB +	Data Pair B+	+Vdc + BI_DB +	power(+)Data Pair B+
4	BI_DC +	Data Pair C+	+Vdc + BI_DC +	power(+)Data Pair C+
5	BI_DC -	Data Pair C-	+Vdc + BI_DC -	power(+)Data Pair C-
6	BI_DB -	Data Pair B-	+Vdc + BI_DB -	power(+)Data Pair B-
7	BI_DD +	Data Pair D+	-Vdc + BI_DD +	power(-)Data Pair D+
8	BI_DD -	Data Pair D-	-Vdc + BI_DD -	power(-)Data Pair D-

Note : the model is isolated design, the output +/- or input +/- can be shorted to ground (FG).

8. CONNECTOR FIGURE :

Input : HD-515RC (P2)





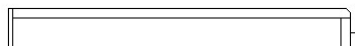
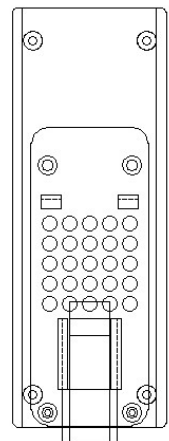
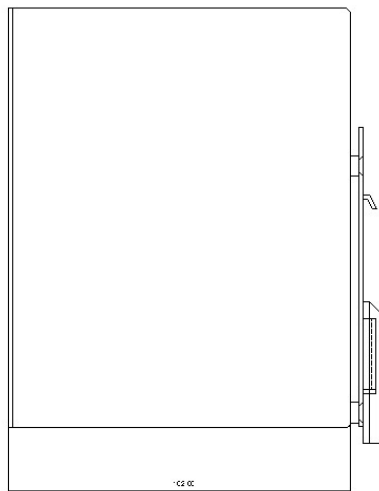
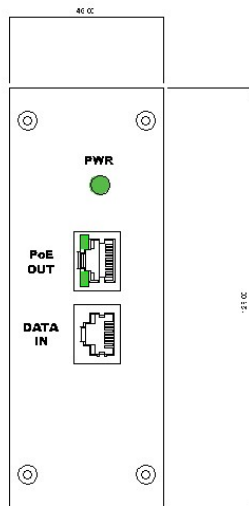
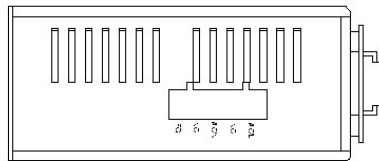
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